

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Carl M. Panasik, Et al.

Docket No.: TI-32958

Serial No.:

09/846,841

Examiner:

TBD

Filed:

04/30/2001

Art Unit:

TBD

For:

Wireless User Terminal and System Having

Conf. No.:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an

envelope addressed to: Assistant Commissioner For Patents,

4986

Signal Clipping Circuit for Switched Capacitor Sigma Delta Analog to Digital Converters

TRANSMITTAL OF FORMAL DRAWINGS

July 6, 2001

Assistant Commissioner For Patents

Attn: Official Drafts Person Washington, DC 20231

Dear Sir:

Submitted herewith are <u>5</u> sheets of formal drawings.

The enclosed drawings are being submitted in a timely manner, therefore no additional fee is required.

Respectfully submitted,

On O. Mus

Texas Instruments Incorporated

P. O. Box 655474, MS 3999

Dallas, Texas 75265

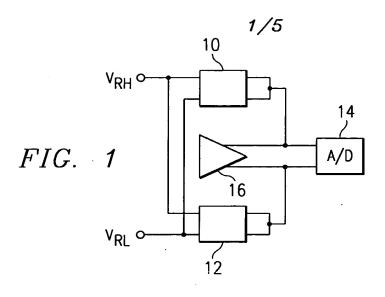
(972) 917-5299

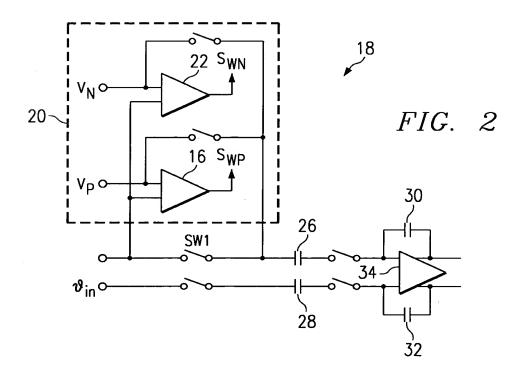
Fax: (972) 917-4418

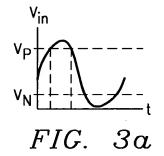
Ronald O. Neerings

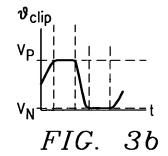
Attorney for Applicants

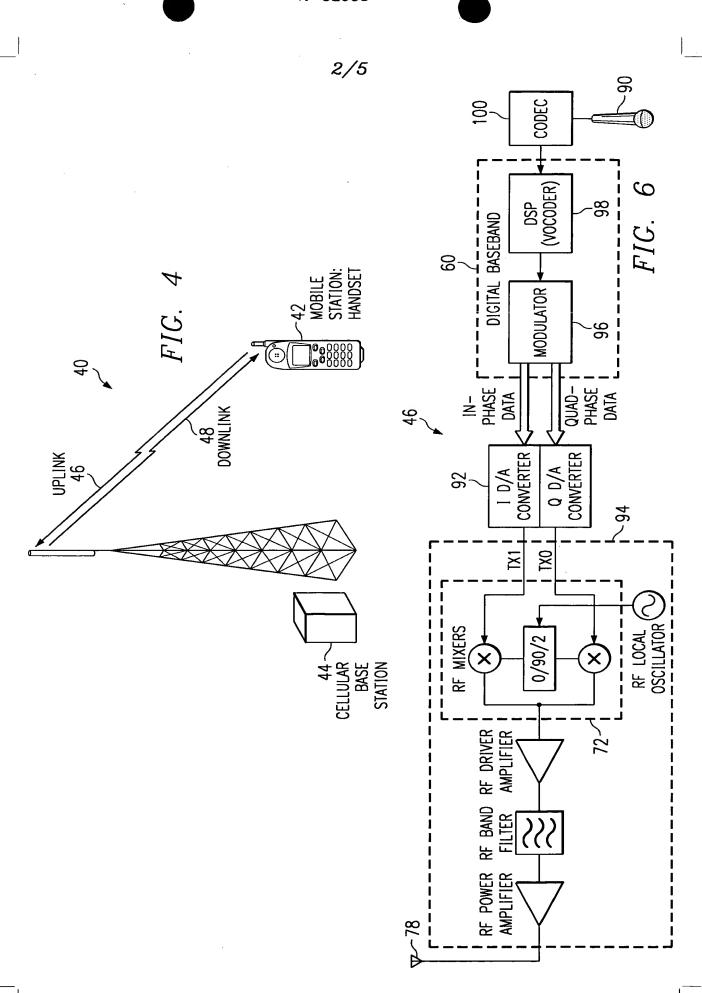
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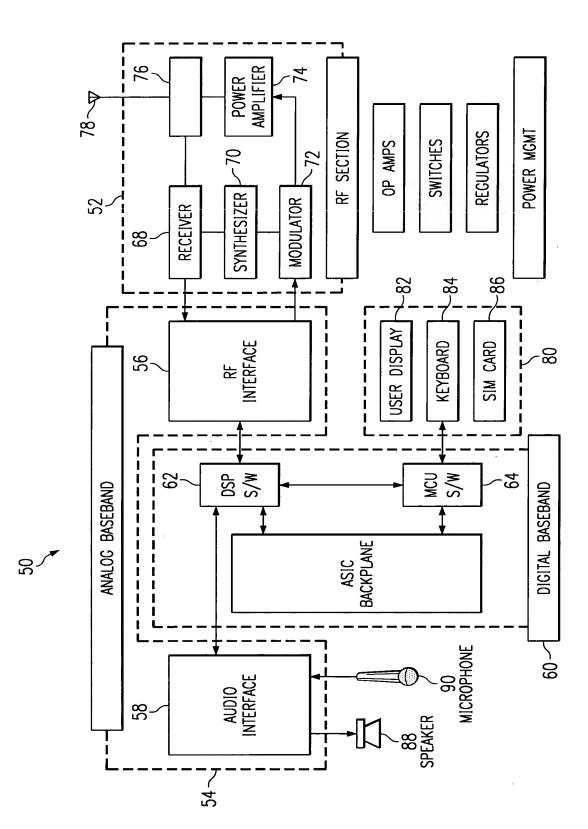
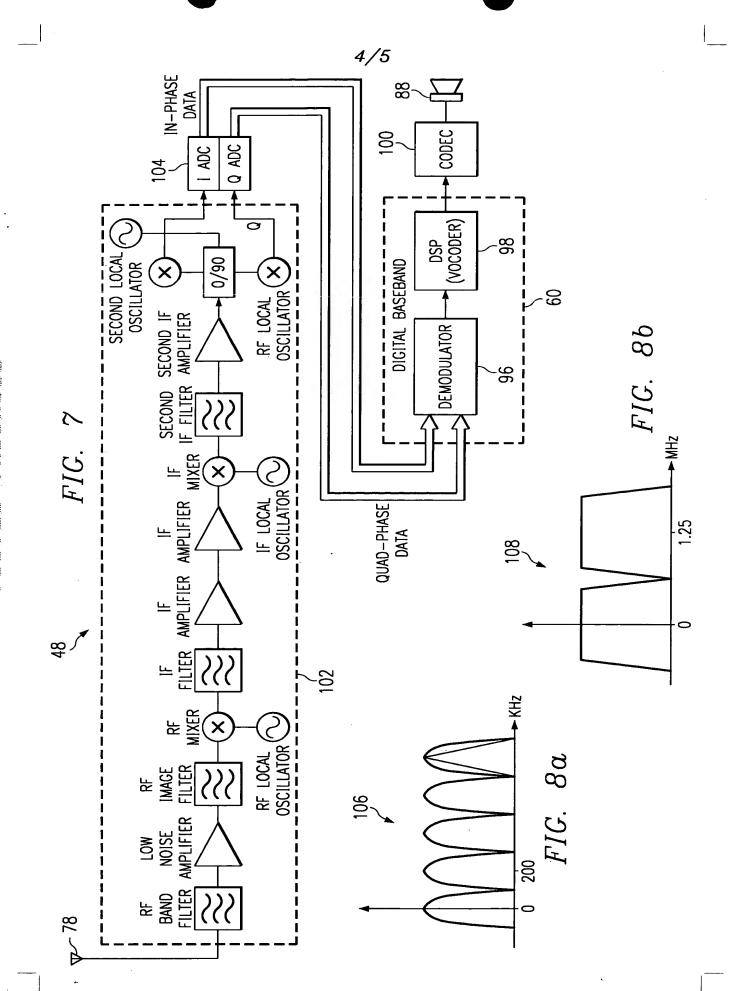


FIG. 5



DOSKSTI JOZIOI

DOBTEDLA COLLOCA